Ruggedized Conduction Cooled ATDS PMC

Specifications

GET Part #
10075601

ATDS Interface
Full Compliance to MIL-STD-188-203-1A, Appendix D2

Form Factor
Front I/O Standard Single CCPMC Card Module (74 mm x 144 mm)

Rear I/O Standard Single CCPMC Card Module (74 mm X 144 mm)

Bus Interface
IEEE 1386.1-2002 Standard for common mezzanine card family

Power Requirements
+5VDC @ 200mA

ATDS I/O connectors
Front Panel
Micro-D-Subminiature 25-pin plug (MIL-STD-83513 optional)

Rear Panel (P4)
P4 I/O per ANSI/VITA-35-2001

Temperature

Standard
0°C to 55°C Operating (MIL-STD-810, Method 501 and 502, Procedure II)

Extended
-40°C to 85°C Operating (ANSI/VITA 47, Class CC4)

Configuration Options
Bus Interface Options
cPCI Carrier
PCI Carrier
PCIe Carrier

ATDS I/O Mezzanine Options
Micro-D 25-pin
Rear I/O—P4

GET Engineering is proud to announce its new MIL-STD-188-203-1A (ATDS) Conduction Cooled Interface Adapter fully compliant to the ANSI/VITA-20-2001 Conduction Cooled PMC Standard. This adapter provides a TADIL-A Serial Interface functioning either as a Data Terminal Set (DTS) or Terminal Data System (TDS). All configuration parameters are accessible through a simple software interface with FPGA-controlled DMA channels to reduce host CPU overhead.

GET Engineering's ATDS card is available in either commercial or extended temperature ranges with the option of either front panel (Micro-D 25 pin) or Rear (P4) I/O. The software included with this adapter card provides the user with an operating system independent API enabling easy migration from one operating system to another; for example, from Windows to Linux.

9350 Bond Avenue • El Cajon, CA 92021 • Tel: 619.443.8295 • Fax: 619.443.8613 • www.getntds.com

Ruggedized Conduction Cooled ATDS PMC

Environmental Specifications

Operating Temperature Range
0°C to 55°C Operating (MIL-STD-810, Method 510 and 502, Procedure II)

Industrial Temperature Range Option
-40° to 85°C Operating (ANSI-VITA 47, Class CC4)

Storage Temperature
-40° to 85°C Operating (MIL-STD-810 Method 510 and 502, Procedure I)

Humidity
5 to 95% humidity operational (non-condensing) (MIL-STD-810, Method 507)

Vibration
0.01g²/Hz 15-2 KHz, Optional 0.1g²/ HZ 15-2KHz, (MIL-STD-810, Method 514, Procedure I)

Operating Shock
20 g peak, Optional 40 g Peak, (MILSTD-810, Method 516, Procedure I)

Mean Time Between Failures (MTBF)
> 250K Hours per MIL-HDBK-217, Rev E, 25°C Ground Benign Environment

Key Features

- Common API across multiple operating systems
- Memory Mapped Address Space
- Independent FPGA-controlled DMA Channels
- Extensive Built-in-Test capabilities enable rapid troubleshooting of interface
- Sample code provided for rapid Application Code Development

Key Software Features

- ANSI/VITA 20-2001 compliant PMC Module
- Data Frames of 13.33 or 22 milliseconds
- Supports Picket Addressing and Sidetone
- User controlled Front Panel LED for Adapter Status
- Command and Inter-word time-outs
- Front or Rear Panel (P4) I/O configurations available

9350 Bond Avenue • El Cajon, CA 92021 • Tel: 619.443.8295 • Fax: 619.443.8613 • www.getntds.com